

Docket Q02-1031-US1



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of: ) Group Art Unit: 2112  
Thomas Bolt, et al )  
Serial No.: 10/075,121 )  
Filed: Feb. 13, 2002 )  
For: Use of the Universal Serial Bus as an )  
Internal Architecture within IDE Disk )  
Array )  
Examiner: Patel, Nimesh  
**Reply to Final Action**  
**Dated January 27, 2006,**  
**and Interview Summary**

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Reply

MS AF  
Commissioner for Patents  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

Dear Sir/Madam:

Applicant hereby responds to the Final Action of January 27, 2006 in the above-referenced patent application. Reconsideration and allowance of the claims is respectfully requested in view of the following amendments and remarks. The undersigned has been authorized to respond to the Office Action.

The claims are reflected in the listing of the claims which begins on page 2 of this paper.

Remarks begin on page 7 of this paper.